

VLPC Detectors: Central Fiber Tracker & Pre-shower Shift Tutorial

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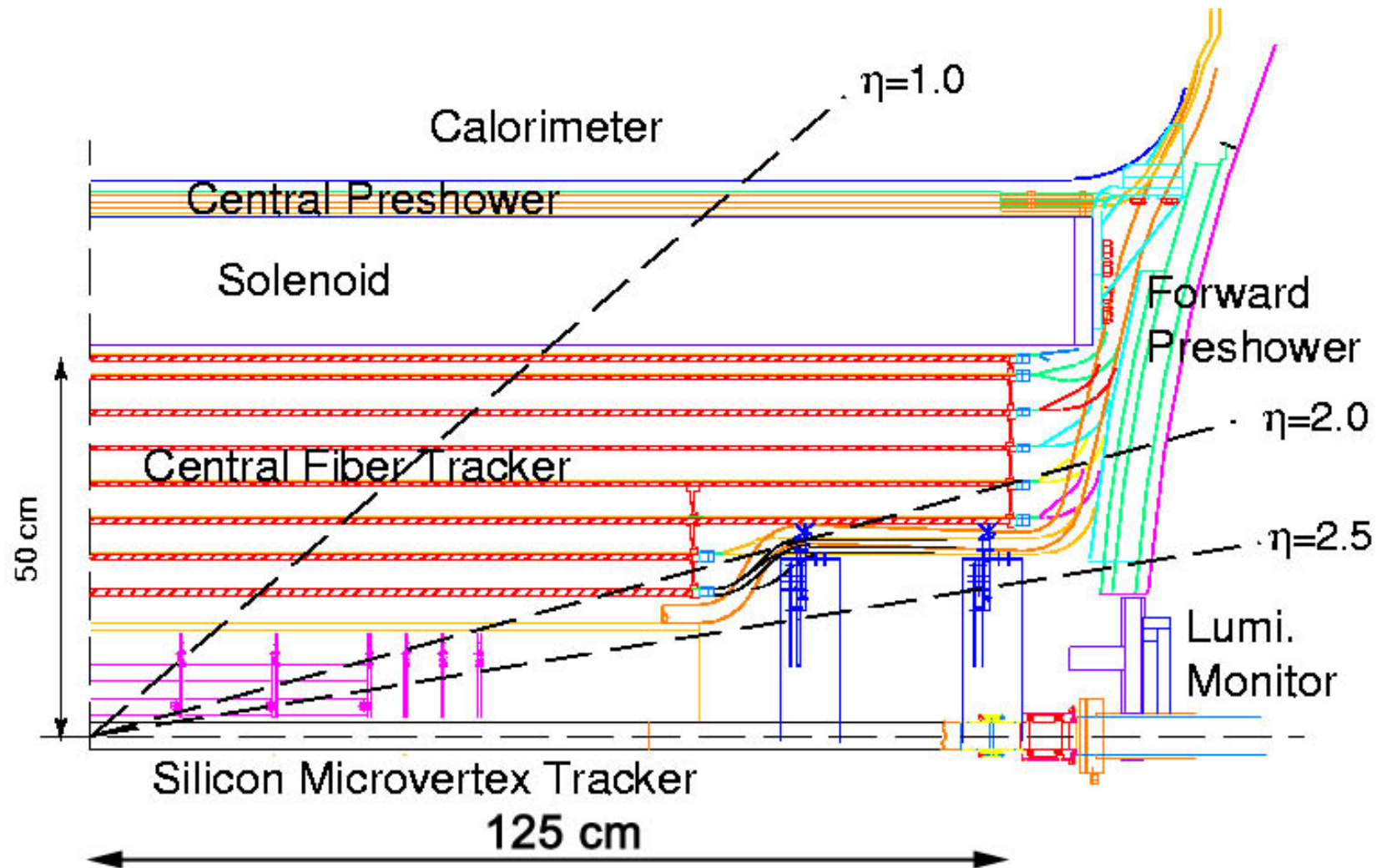
May 7, 2007

- CFT & PS provide essential data for the D0 experiment
- CFT & PS collect data usually 7 days a week most of the year during **global, cosmic, and zero-bias runs**
- Tracking shifts are offset by 4 hours from other control room shifts:

| | | | |
|---------------------|----------|---|----------|
| Farmer | 4:00 am | – | 12:00 pm |
| Country Club | 12:00 pm | – | 8:00 pm |
| Vampire | 8:00 pm | – | 4:00 am |

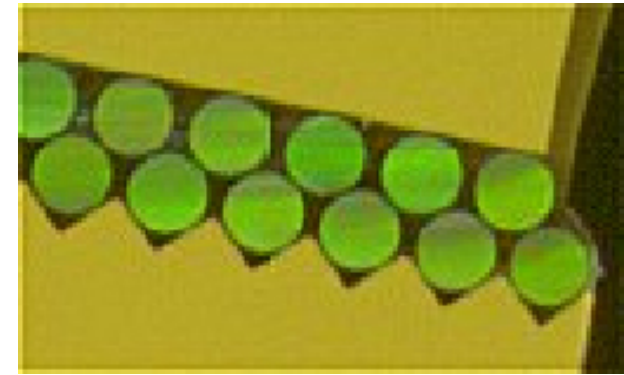
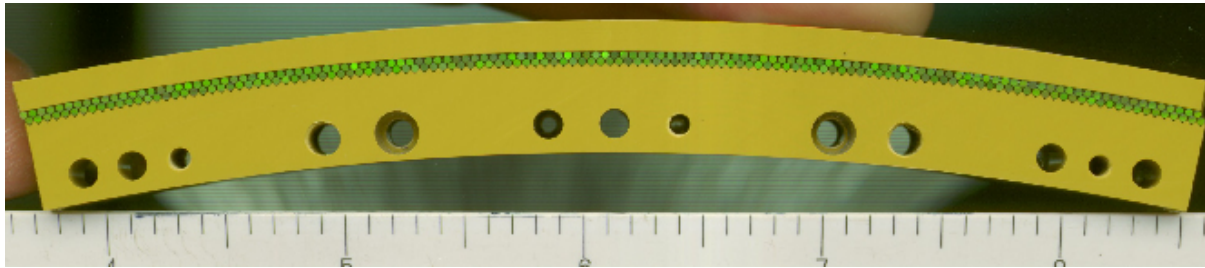
- Shifter required to arrive on shift about 5-10 minutes prior to start of shift to discuss current system status with previous shifter
- **Any shifter** may request extra training shifts if they feel it is needed!

- **Monitor detector's readout and response**
 - Watch for alarms: Bias, temperature, power supplies, DAQ and others
 - Ensure data quality
- **Minimize down time**
 - Fix most problems
 - Page experts when necessary
- Assist experts in taking calibration runs, performing maintenance
- **Understand the detector and optimize it's performance**
- **Feedback on tools needed for shifts**
- Accomplish any extra stated shift goals

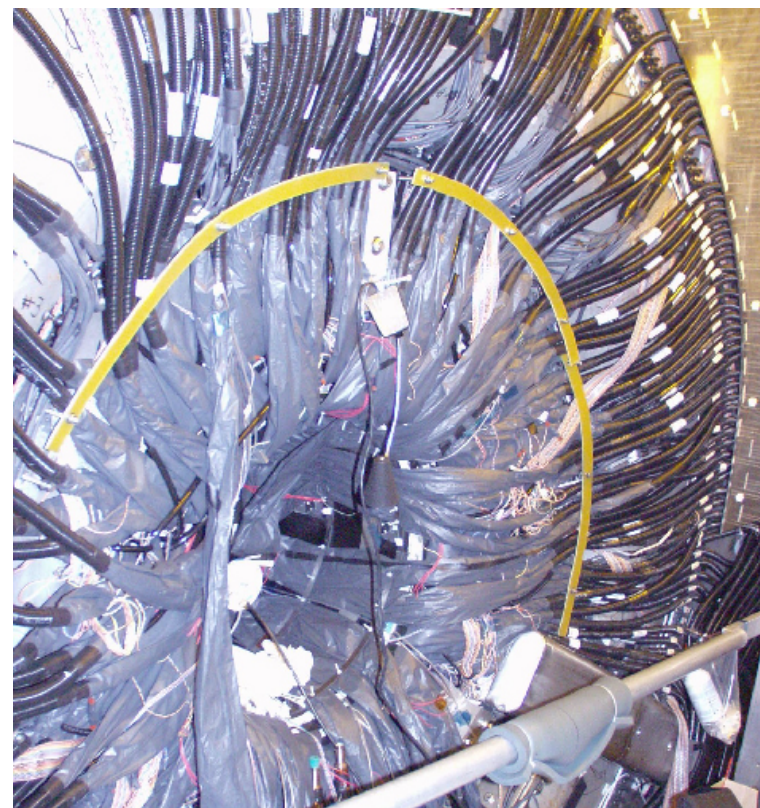
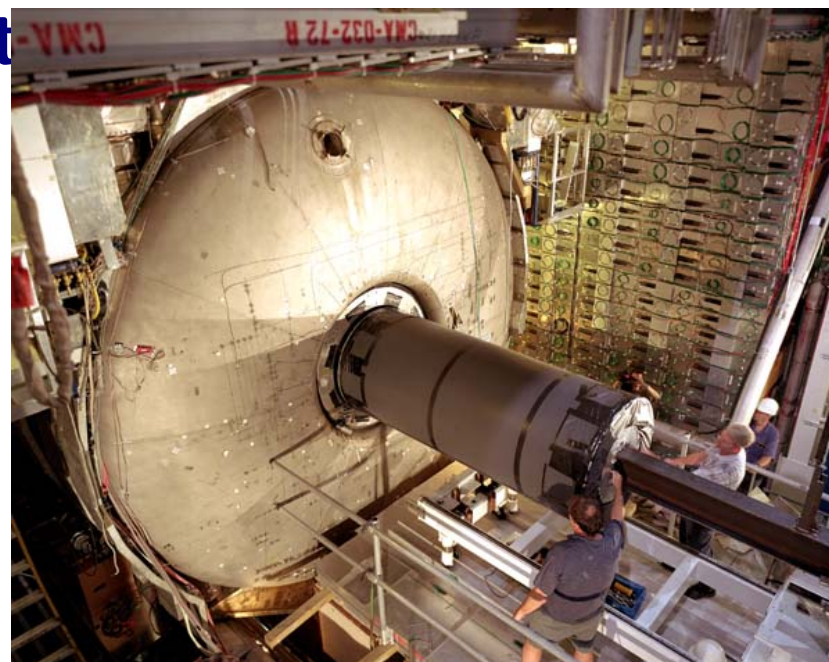
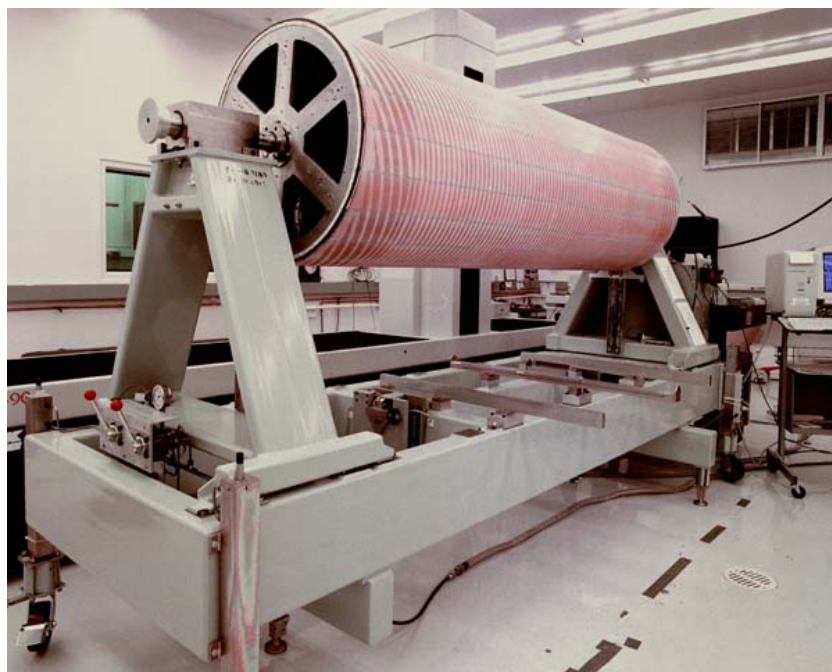


- Actually, 3 detectors:
 - **CFT**: 8 barrels = 8 axial + 8 stereo doublet layers $\approx 77,000$ channels
 - **CPS**: 1 axial + 2 stereo (U, V) stereo layers $\approx 7,000$ channels
 - **FPS**: 2 MIP + 2 shower layers $\approx 15,000$ channels
- CFT and PS utilize similar readout through Visible Light Photon Counters (VLPCs)

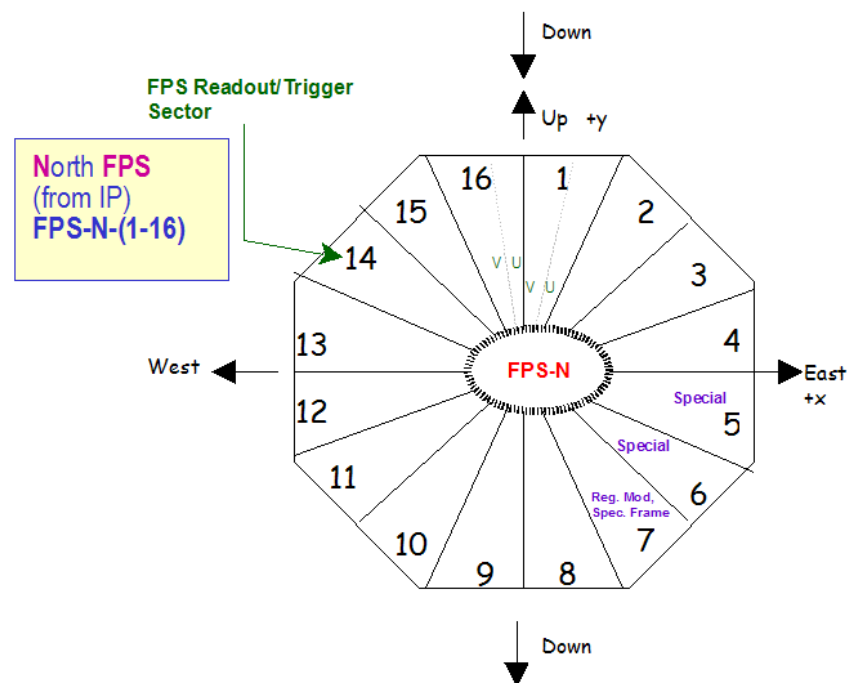
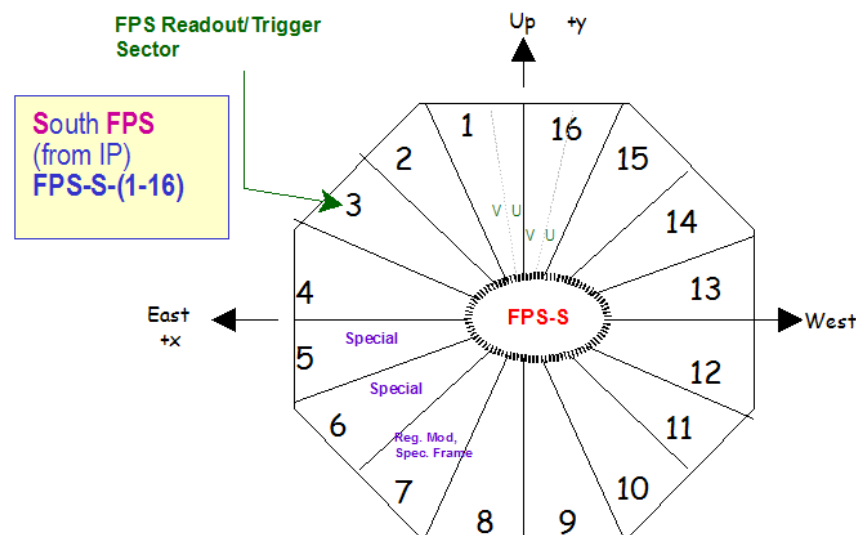
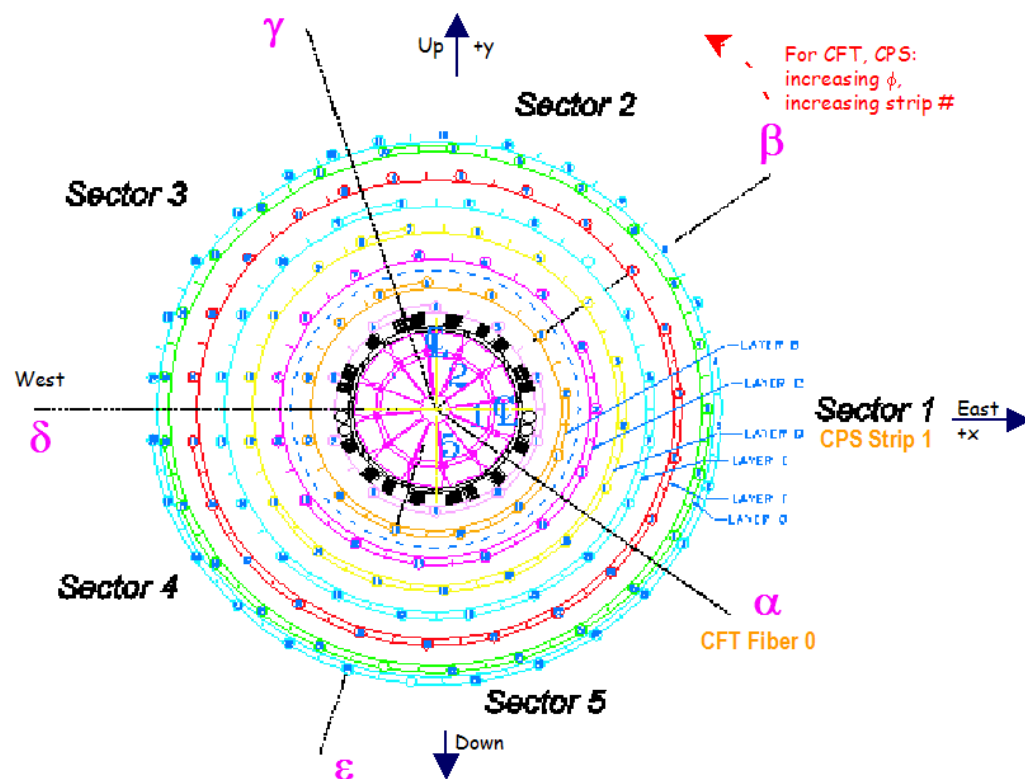
- CFT scintillating fibers arranged into precisely positioned ribbons of interlocked fiber doublets



- Fibers are mounted on outside surface of eight carbon fiber support cylinders
- Axial layers are formed by fibers oriented along the cylinder axis
- Stereo layers are formed by fibers oriented at $\pm 3^\circ$ angle
- position resolution of fiber doublet is *approx* $100 \mu\text{m}$

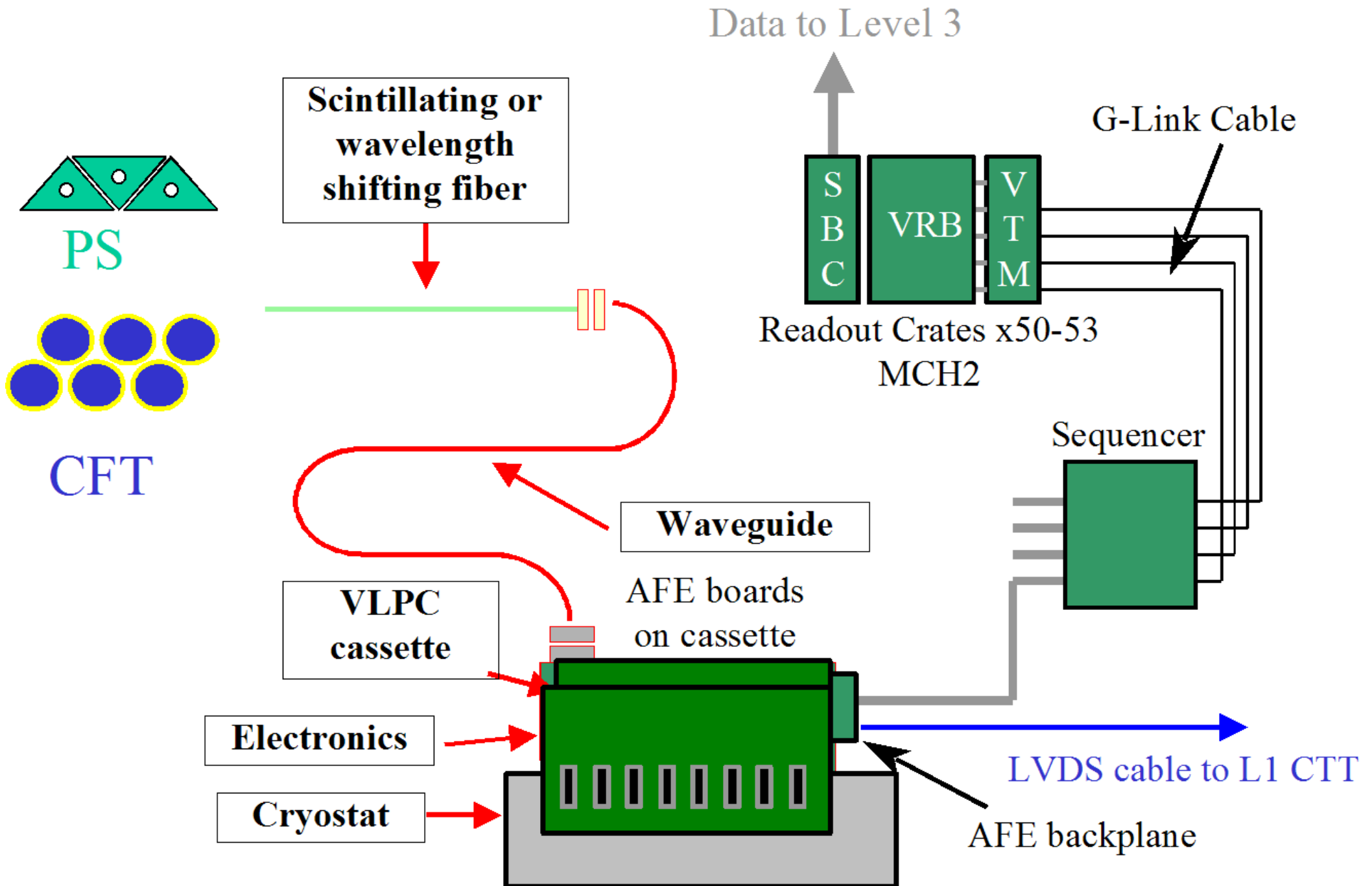


- Scintillating fibers are 1.8 or 2.6 m long
- Waveguides length varies from 8.2 to 11.4 m
- CFT fiber diameter is $835 \mu\text{m}$
- 5 “Super Sectors” in ϕ for the CFT and CPS
- 80 individual sectors in all. Normal CTT examine readout mode.



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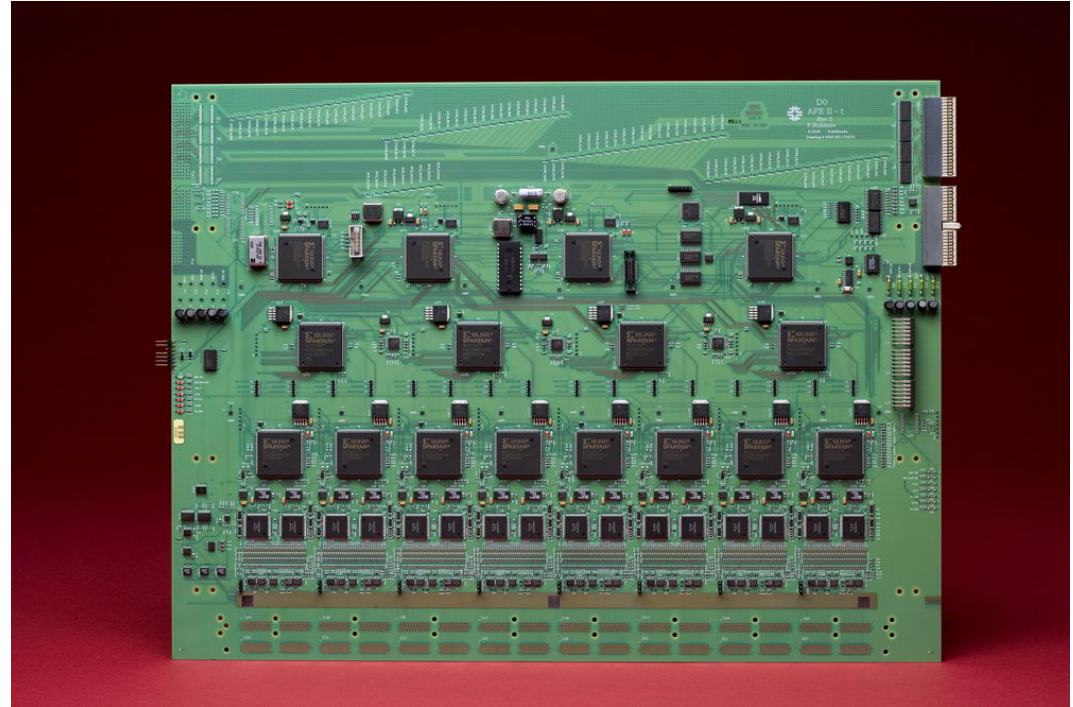
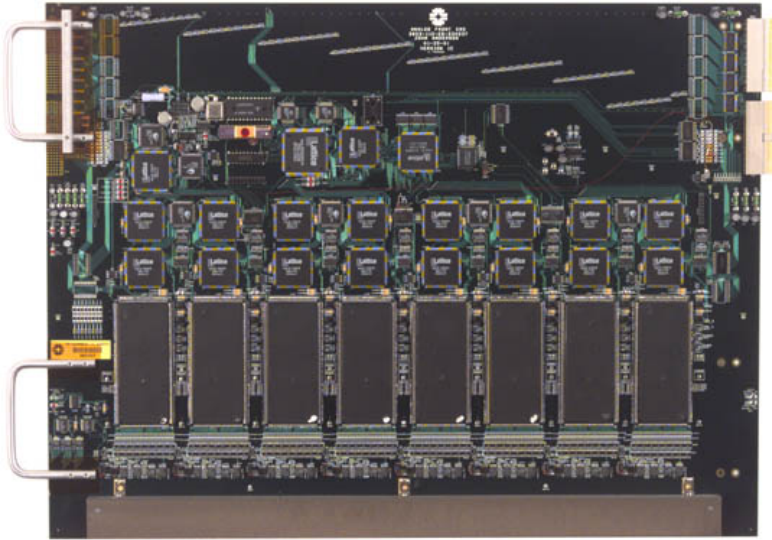




Analog Front-End (AFE) Boards

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- AFEI (left) and AFEII-t (right) shown below



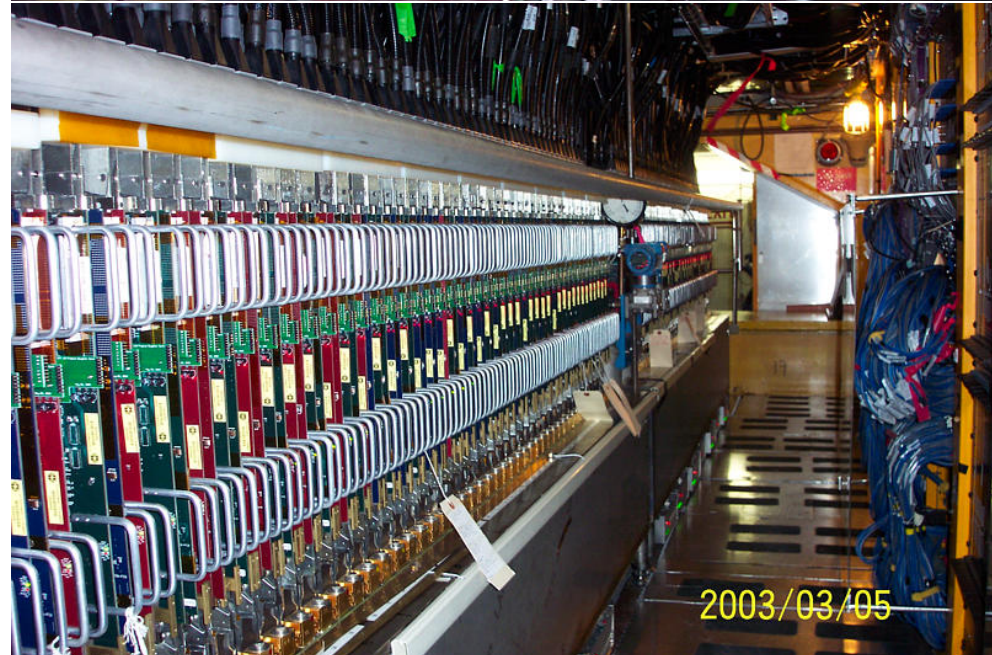
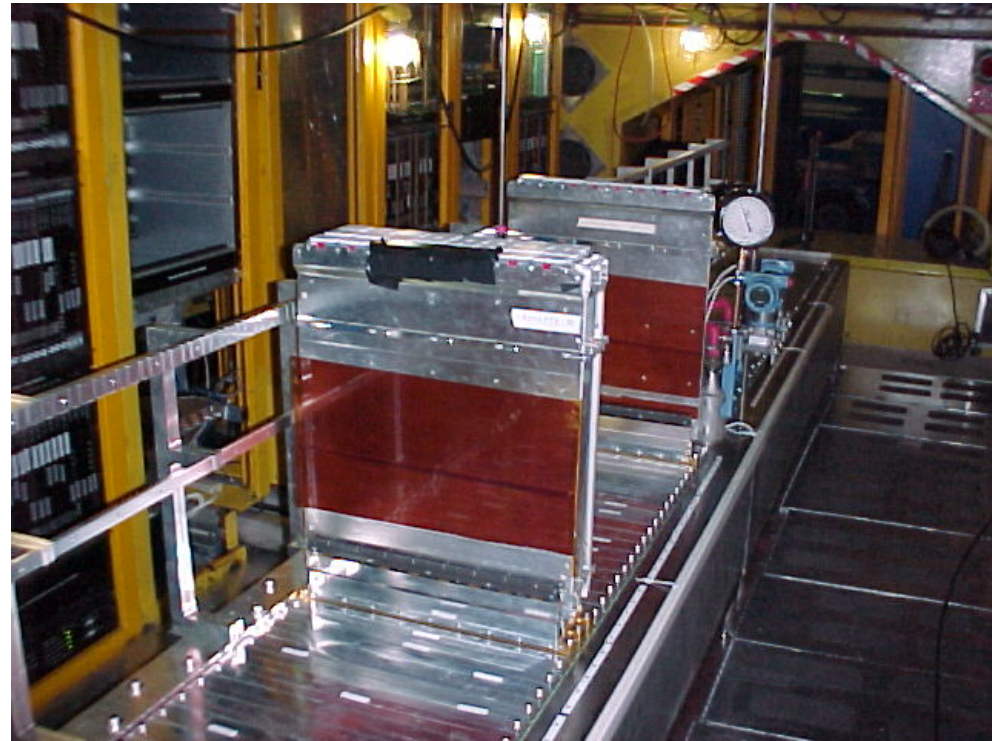
- One board can readout up to 512 channels
- Two kinds of boards: Left-hand and right-hand
- AFE forms discriminated output for trigger and controls VLPC bias voltage
- Only right-hand boards control the temperature of the VLPCs.
Boards installed in pairs in the cryo cassettes

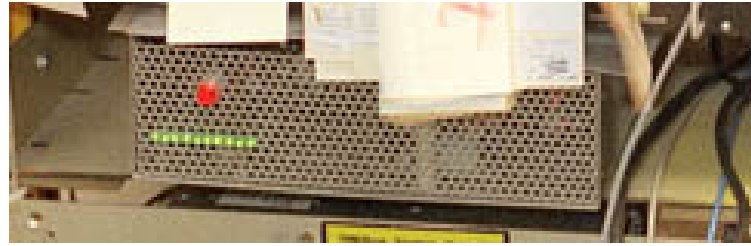
- Cassettes provide mechanical support, optical alignment, and appropriate operating services for proper operation and readout of the VLPCs
- Lower portion immersed in gas Helium
- Upper portion supports a pair of AFE boards
- A mock setup with one cryo cassette is available in DAB3's visitor area. Look on your own or ask an expert for a tour!



Cryostat & AFE Cassettes Pictures

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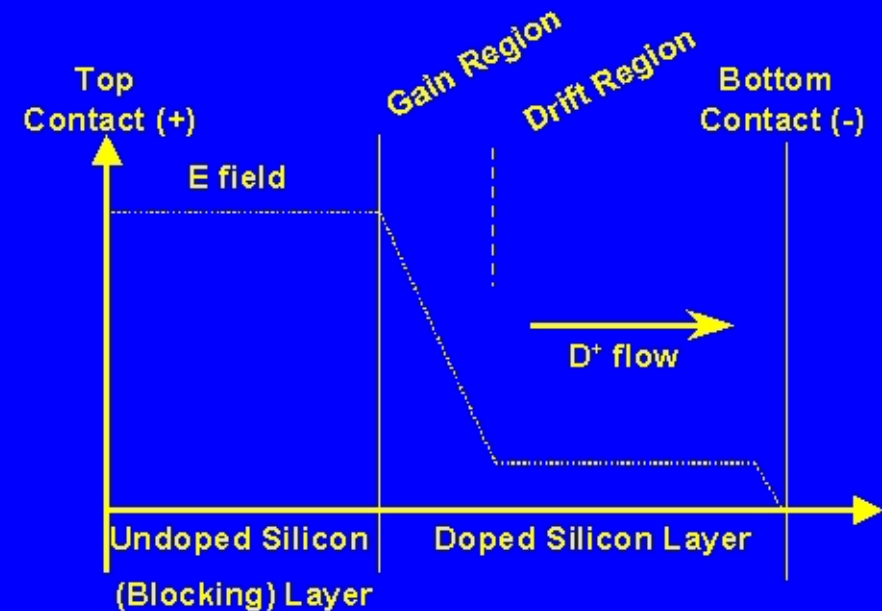
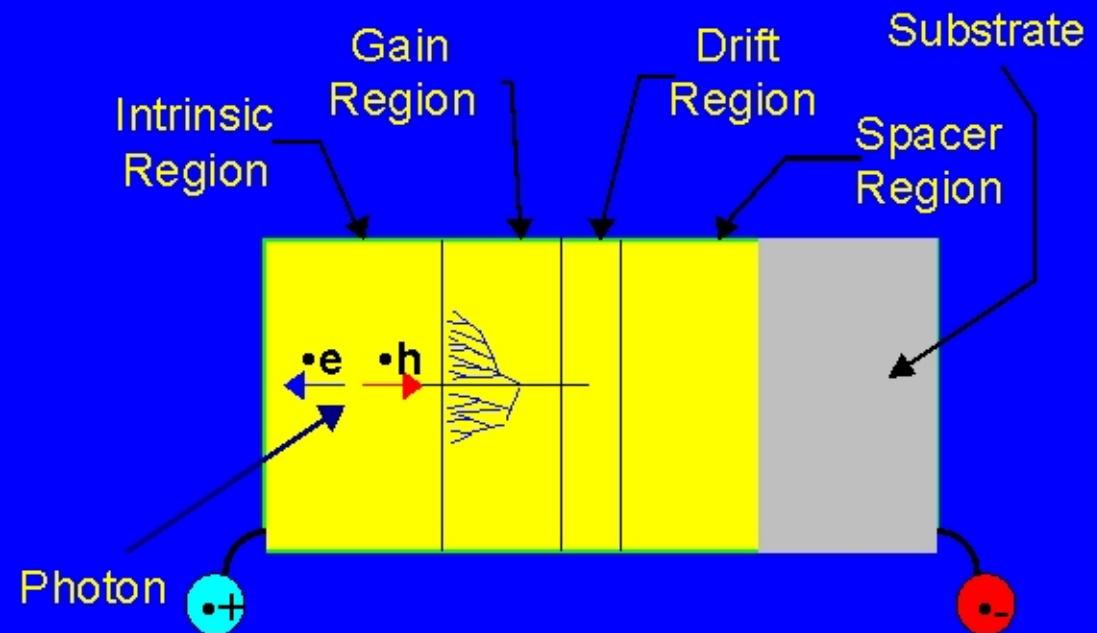




- The AFE boards are powered by thirteen Vicor power supplies under the cryostat
- Each Vicor powers one 'AFE Crate' which contains up to sixteen AFE boards
- The thirteen AFE crates are each divided into A and B halves that can be powered up and down
- These crates also give the AFEs their operational names like '4B0'
4B is the half crate and there are 8 boards, 0-7. Even numbered boards are left-hand
- The PS provides +12, +5.5, +5, +3.3, and -12
- They can be powered remotely from the control room or the platform itself

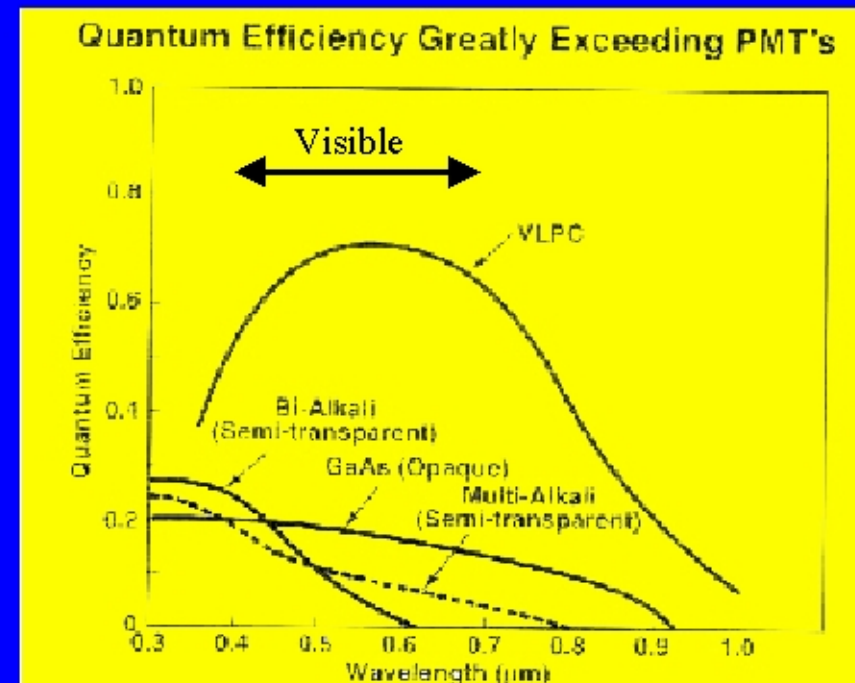
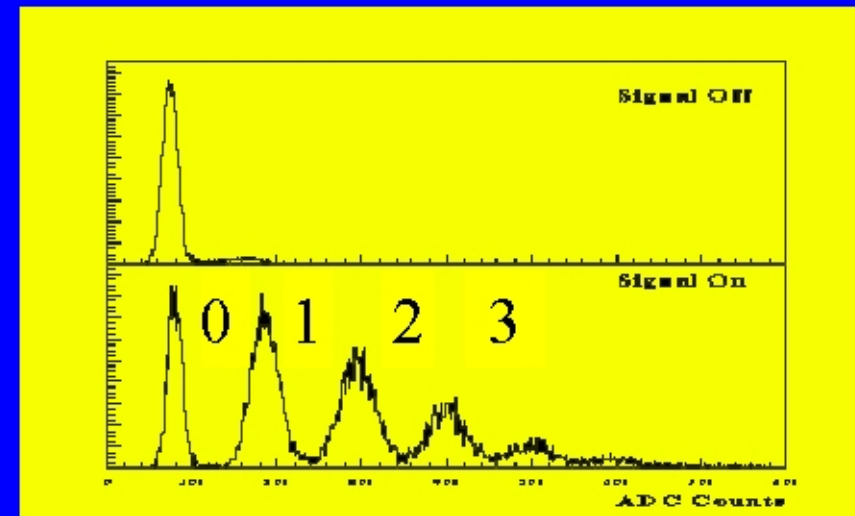
VLPC Operational Principles

- Photon is converted in the intrinsic region, creating an electron-hole pair.
- Hole drifts into the drift region, where it knocks an electron out from an atom.
- Electron accelerates back through gain region, knocking electrons from atoms as it goes.
- Spacer region and substrate are for mechanical support and field shaping.
- Thus each photon generates a pulse of many electrons. Gains of $\times 20,000 - 60,000$ are achievable.

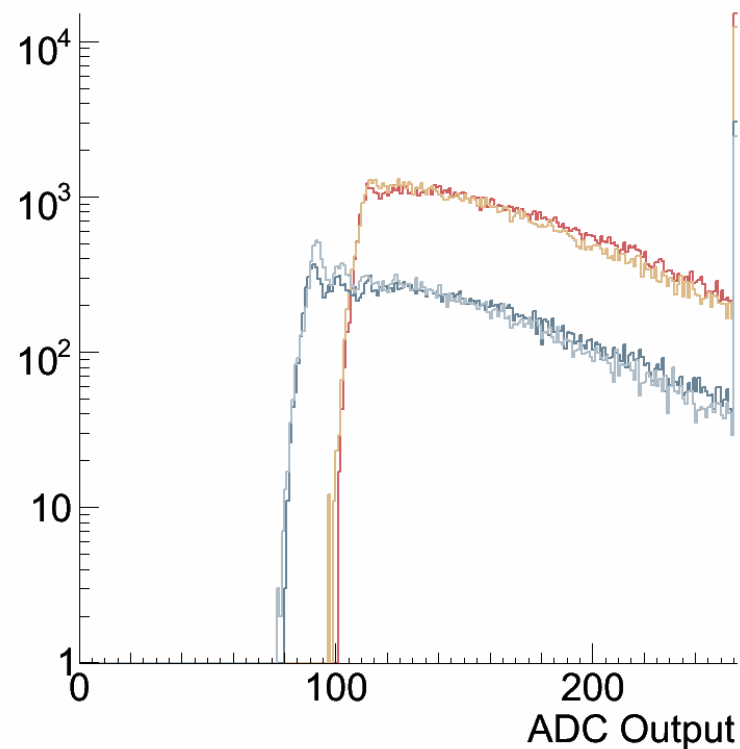
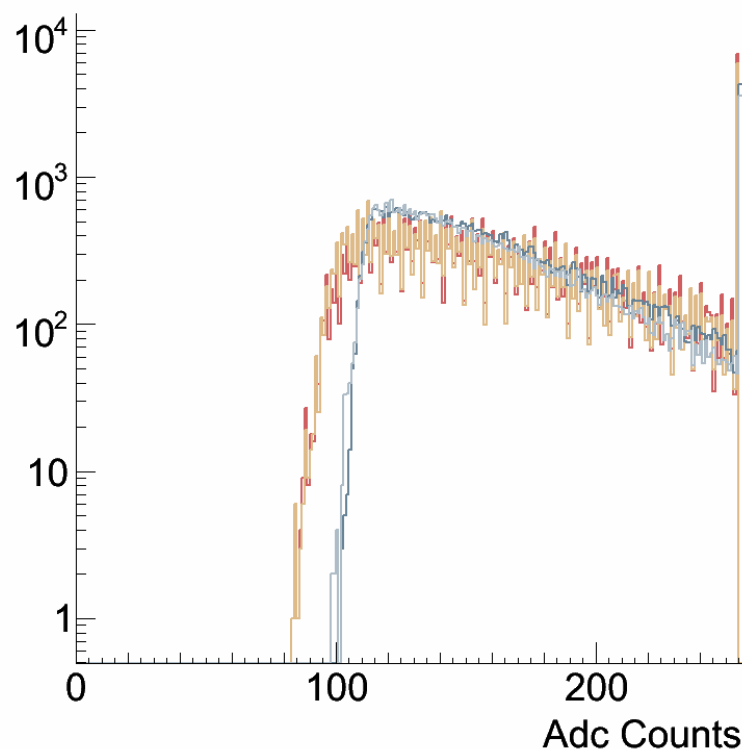


HiSTE VI

- Solid state photon detectors
- Operate at a few degrees Kelvin ($\sim -450^\circ \text{F}$)
- Bias voltage 6-8 Volts
- Detects single photons
- Can work in a high rate environment
- Quantum efficiency for visible light $\sim 80\%$
- High gain $\sim 50\,000$ electrons per converted photon
- Low gain dispersion
- Highly suppressed infrared sensitivity



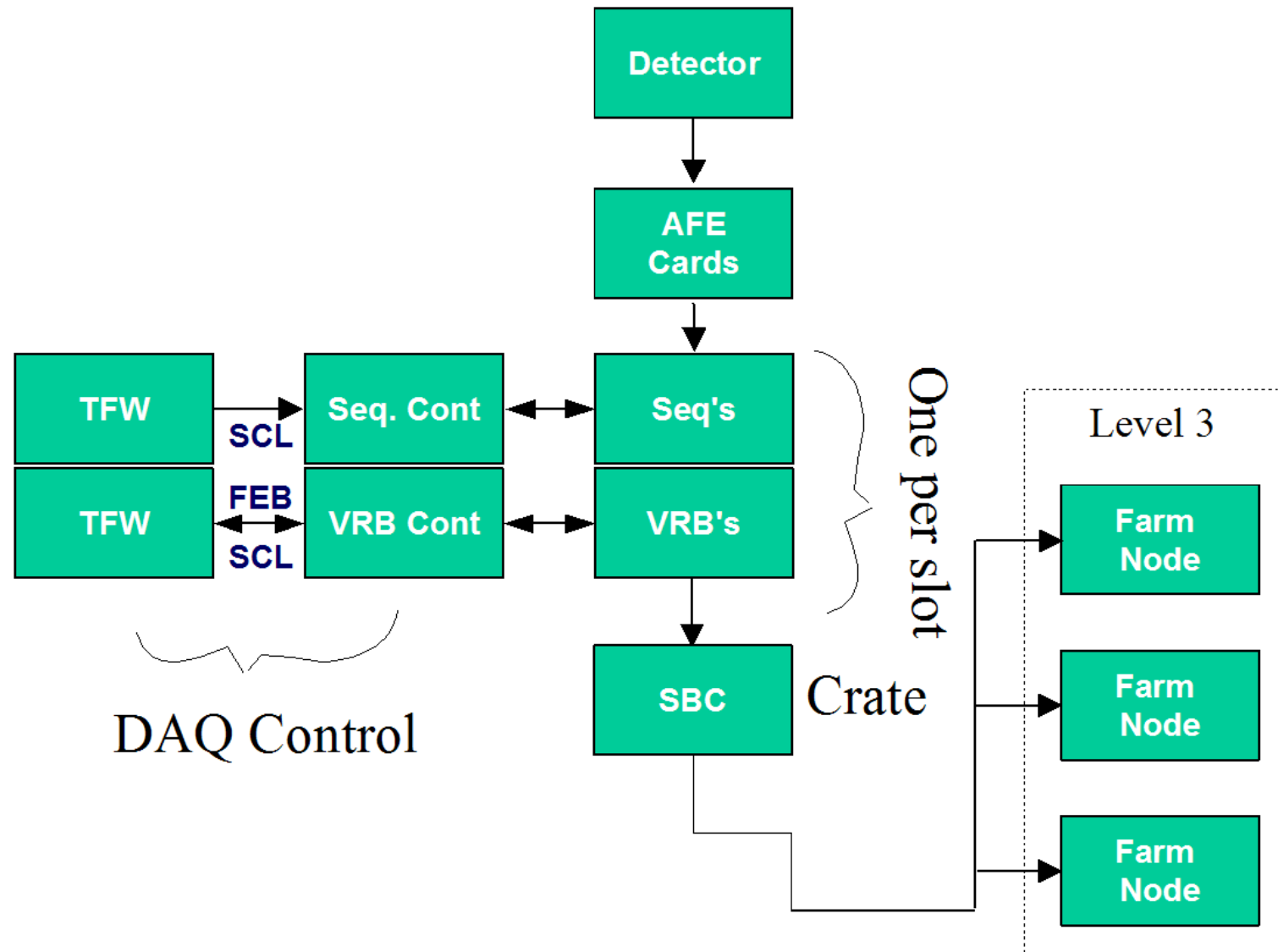
- Each AFE board reads out 512 channels
- All but FPS AFEI boards were replaced with AFEI-t
- Totally redesigned AFEI increases signal efficiency



- AFEIs are more stable, therefore, need less frequent calibrations
- AFEIs are capable of producing the time signal

Primary DAQ Data Flow

- AFE boards controlled by SEQuencers and data is readout to VRBs



- AFE boards, sequencers, and seq. controllers located on the platform
- VRBs, VRBCs, and SBCs located in VRB crates 0x50–0x53 in MCH2

- There are four VRB crates in MCH2 that handle all of our data.

| | | |
|------|--------------|----------------------|
| 0x50 | CFTAX | The entire CFT axial |
| 0x51 | CFTST | Mixed CFTS and CPS |
| 0x52 | CPS | Mixed CFTS and CPS |
| 0x53 | FPS | The complete FPS |

- Each sequencer crate in the pit feeds two VRB crates upstairs
- Each sequencer sends its signals to a VTM which converts the light pulse to an electronic signal on the crate's backplane which is picked up by the corresponding VRB



- Each VRB Crate contains three special units:
 - One VRB Controller (VRBC) that controls the crate
 - One Single Board Computer (SBC) that is our interface with the L3 trigger
 - One Power PC (IOC) that does some controlling of the crate and is the secondary data acquisition path



- **Online Monitoring Software: Examines**
 - FT Examine: CFT, CPS, FPS
 - PDAQ Examine
 - CTT Examine
- **CFT+PS/CTT specific**
 - CFT GUI
 - FEB_Util & Crate x13 VRB Monitor
 - Channel Archiver
 - CTT GUI & DFE Expert GUI
 - Other Specialized Software (e.g., AFE Monitor, A/O Term Grabber)
- **DAQ tools**
 - Taker SDAQ (mostly used by experts for calibration, and pedestal Runs)
 - Daq monitor
 - l3x_qt_display + DAQ_Dialog
 - Coormon
 - Electronic Logbook (e-log)
 - Significant Event Server (SES) Alarm System

- The CFT GUI is used for downloading various parameters into the hardware
- Calibration runs are configured using the CFT GUI
- The CFT shifter can use it to diagnose and fix alarms

The screenshot displays the CFT GUI interface. At the top, there is a row of control buttons: 'led_power', 'debug: off', 'global parameter', 'update EPICS', 'feb_util', 'close LogFile', and 'quit'. Below this, a table lists various hardware components and their associated actions:

| Component | download | feb_util | reinit VME | on TmpCU | plot ThreshVrefVthres | plot cryo | plot bias | dld+AFEII init | thrsToAfpaga | details | | |
|-----------|----------|-----------|------------|----------|-----------------------|-------------|-------------|----------------|--------------|------------|----------------|---------|
| VRBCR_50 | | | | | | | | | | | | |
| VRBCR_51 | | | | | | | | | | | | |
| VRBCR_52 | | | | | | | | | | | | |
| VRBCR_53 | | | | | | | | | | | | |
| CTS | download | init SDAQ | begin SDAQ | end | insert thrs | insert peds | insert vref | plot bias | prgRNDM | prgVER1213 | dld+AFEII init | details |

Below the table, a diagram shows the hardware configuration for VRBCR_50. It includes a block for VRBC_50, which is connected to VBD_50, which in turn is connected to SEQC_03B01. This sequence is followed by four AFE crates: AFE crate 6, AFE crate 7, AFE crate 8, and AFE crate 9.

The 'Channel chain' section displays a detailed view of the data flow. It shows a series of VRB (Vertex Readout Board) units, each connected to a SEQ (Sequence) unit, which then connects to a Collector AFE (Analog Front End) unit. The units are color-coded: VRB units are green, SEQ units are yellow, and Collector AFE units are blue. The chain is organized into four main groups, each with four VRB units and one SEQ unit:

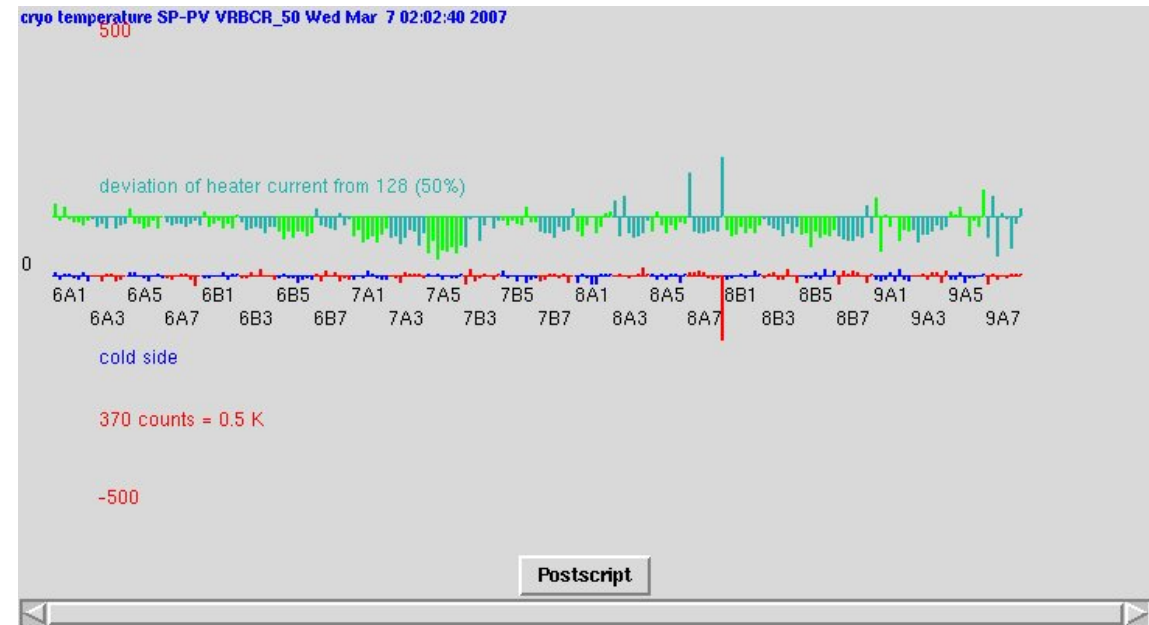
- Group 1 (VRB-5012):** VRB-5012 1, 2, 3, 4 connected to SEQ-03B13, which connects to Collector AFE 6A0, 6A1, 6A2, 6A3, 6A4, 6A5.
- Group 2 (VRB-5013):** VRB-5013 1, 2, 3, 4 connected to SEQ-03B14, which connects to Collector AFE 6A6, 6A7, 6B0, 6B1, 6B2, 6B3, 6B4, 6B5.
- Group 3 (VRB-5015):** VRB-5015 1, 2, 3, 4 connected to SEQ-03B16, which connects to Collector AFE 6B6, 6B7, 7A0, 7A1, 7A2, 7A3, 7A4, 7A5.
- Group 4 (VRB-5010):** VRB-5010 1, 2, 3, 4 connected to SEQ-03B17, which connects to Collector AFE 7B2, 7B3, 7B4, 7B5, 7B6, 7B7, 8A0, 8A1.

Additional groups are shown for VRB-5017 and VRB-5011, each with four VRB units and one SEQ unit, connected to Collector AFE units 8A2 through 8B7.

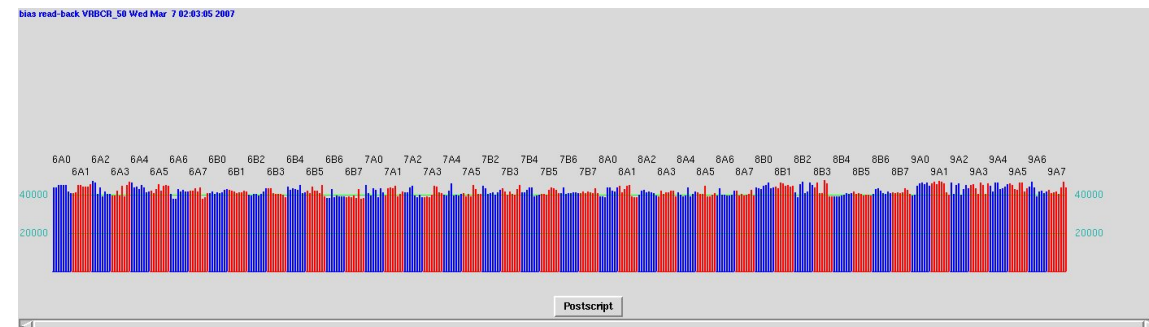
- Some boards may be disabled. Disabled boards are shown in yellow

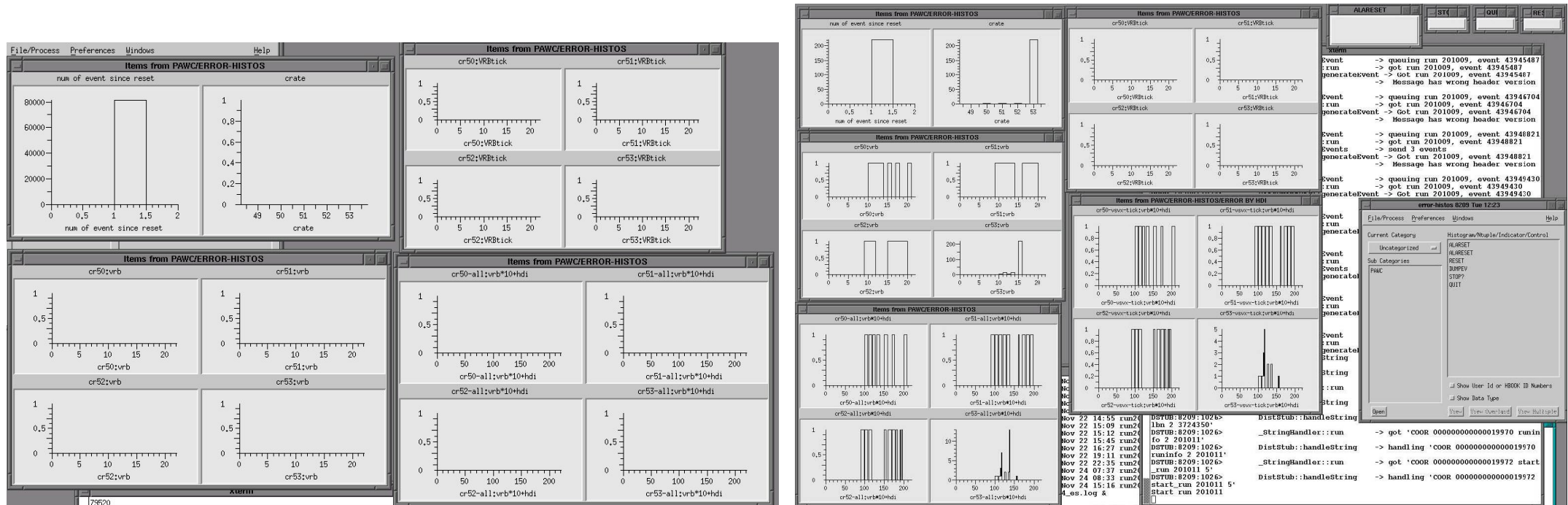
| | | | | | | | |
|--------------|----------------|-----------|-------------|-----------------------|-----------------------|--------------|--------------|
| VRB_5218 | VRB_5218 1 | SEQ_03A07 | SEQ_03A07 3 | Collector AFE_10B0 | Collector AFE_10B1 | AFE_10B0 | AFE_10B1 |
| | VRB_5218 2 | | SEQ_03A07 4 | Collector AFE_10B2 | Collector AFE_10B3 | AFE_10B2 | AFE_10B3 |
| FPD_VRB_5219 | FPD_VRB_5219 1 | SEQ_03A02 | SEQ_03A02 1 | SVXs: on FPD_AFE_14A0 | SVXs: on FPD_AFE_14A1 | FPD_AFE_14A0 | FPD_AFE_14A1 |
| | FPD_VRB_5219 2 | | SEQ_03A02 2 | SVXs: on FPD_AFE_14A2 | SVXs: on FPD_AFE_14A3 | FPD_AFE_14A2 | FPD_AFE_14A3 |
| | FPD_VRB_5219 3 | | SEQ_03A02 3 | SVXs: on FPD_AFE_14A4 | SVXs: on FPD_AFE_14A5 | FPD_AFE_14A4 | FPD_AFE_14A5 |
| | FPD_VRB_5219 4 | | SEQ_03A02 4 | SVXs: on FPD_AFE_14A6 | SVXs: on FPD_AFE_14A7 | FPD_AFE_14A6 | FPD_AFE_14A7 |

- 'plot cryo' button invokes a window with the temperature of VLPCs and the heater currents

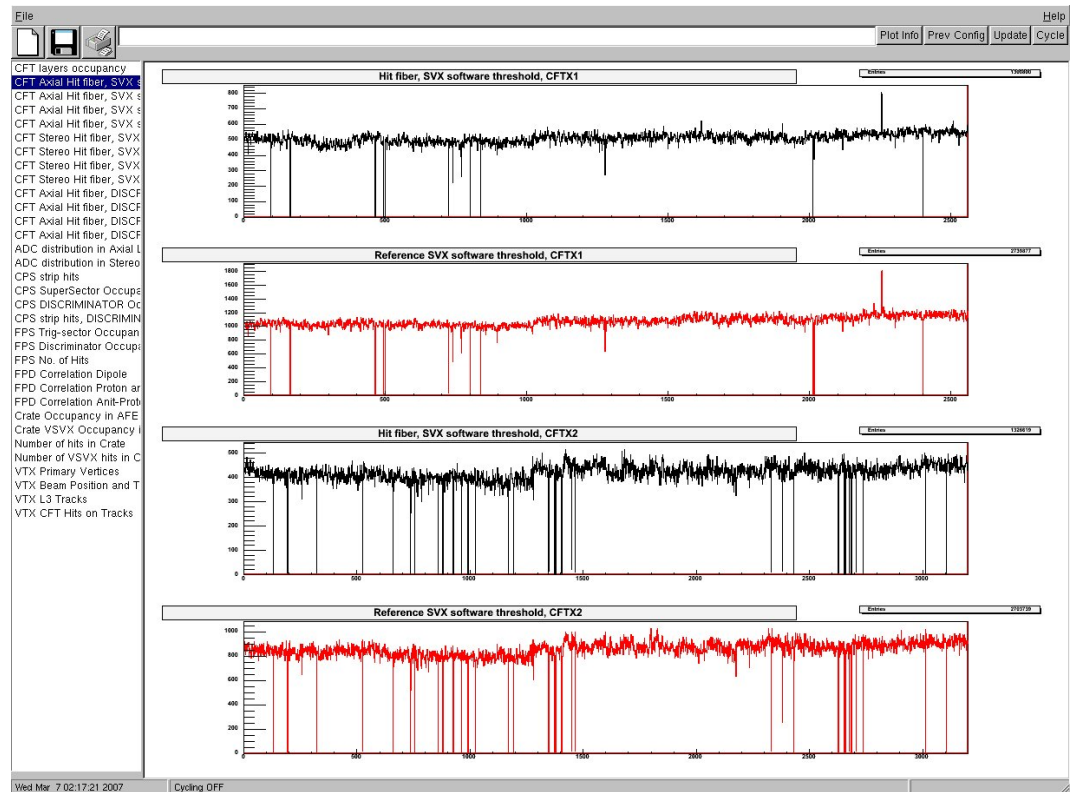
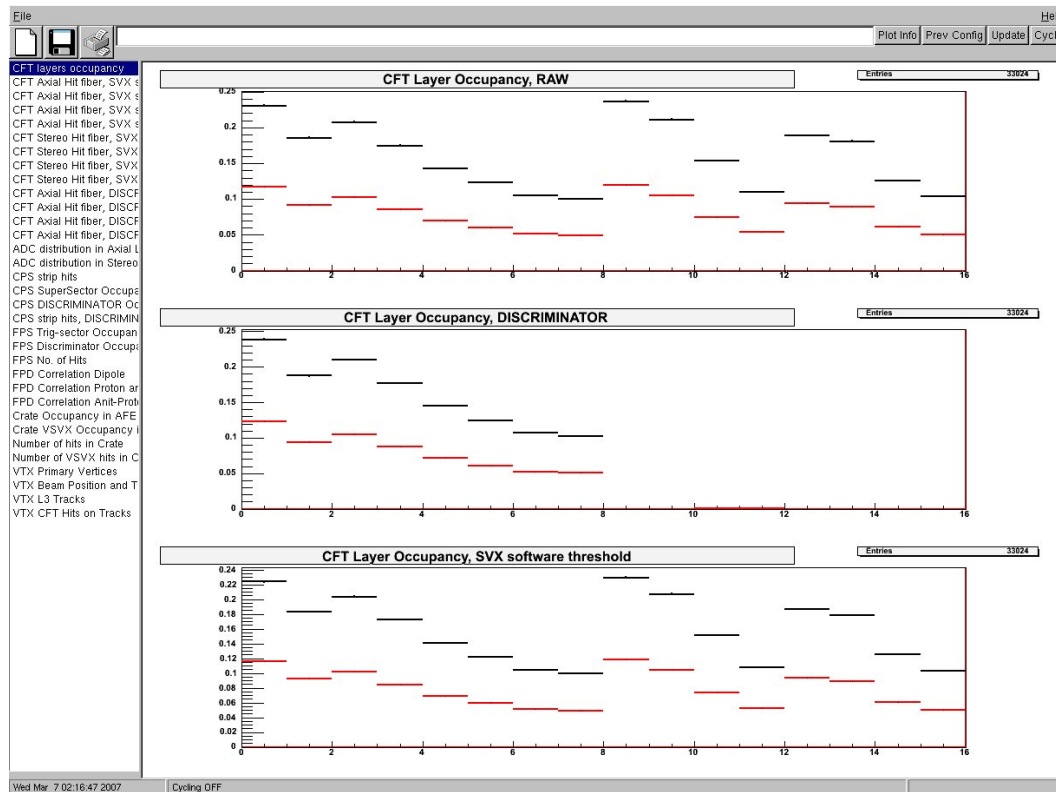


- 'plot bias' button invokes a window with the bias voltages on the VLPCs

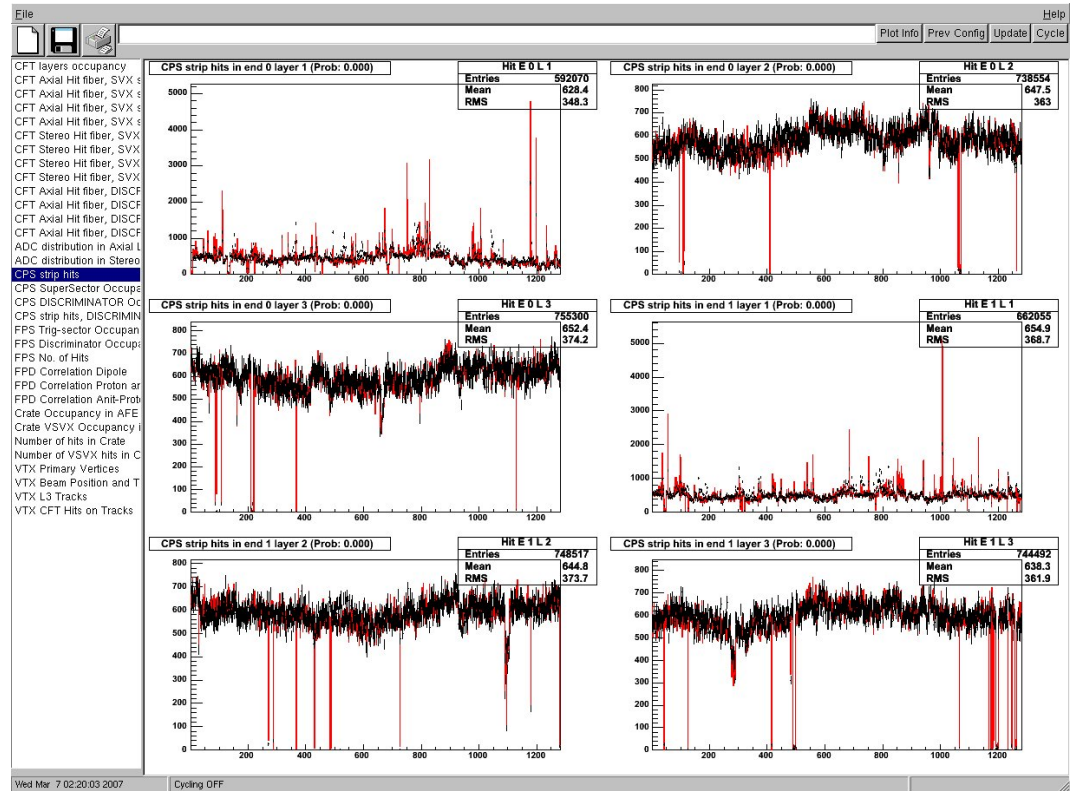
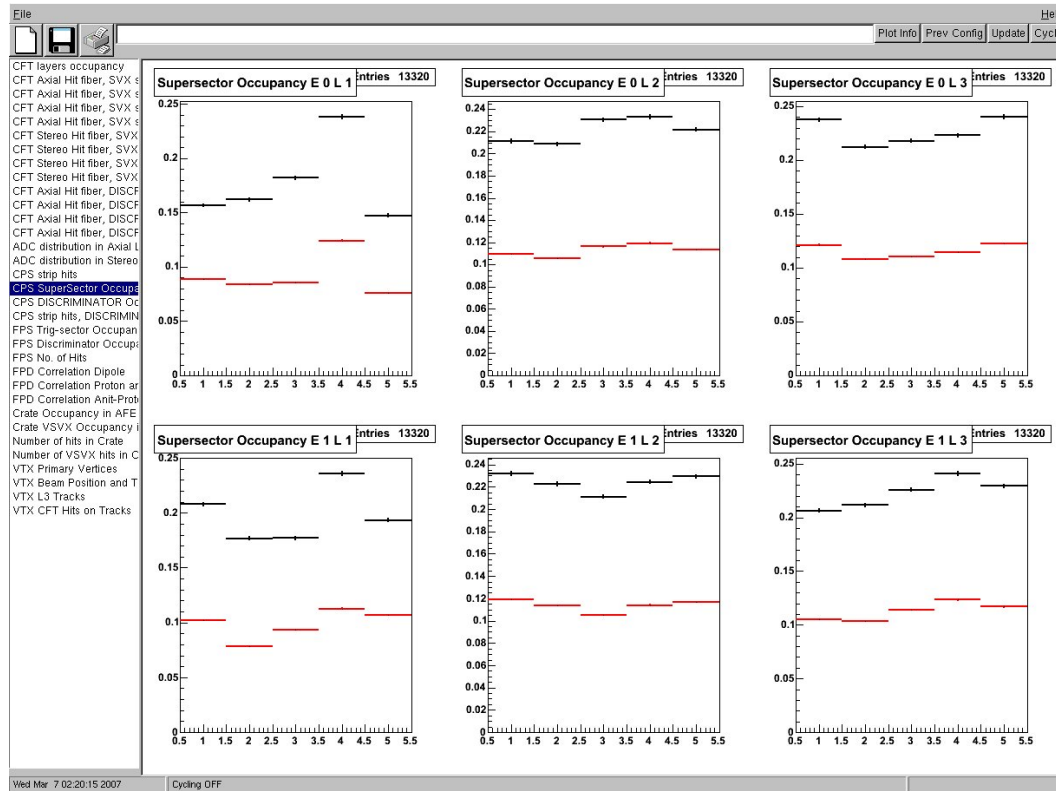




- The PDAQ Examine tracks many kinds of errors that can occur in the data from our crates. Certain errors will cause alarms in the significant event system. Use the 'ALARESET' button to turn these alarms off once the problem has been addressed!



- Occupancy depends on luminosity
- Hitmaps should look reasonably flat
- Do not worry about individual hot or cold fiber
- A new hot or cold region is a real problem



- The same as for the CFT

CFT/CTT Alarm Display

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Main Alarm Display Table:

| | MAJOR | MINOR | INVALID | ACKED | DISABLED | GOOD |
|------------------------------|-------|-------|---------|-------|----------|------|
| Power Supplies | 0 | 7 | 0 | 0 | 4 | 4 |
| Temperature Control | 0 | 1 | 0 | 0 | 8 | 0 |
| Bias Voltages | 0 | 19 | 0 | 0 | 0 | 0 |
| Sequencer and Controller | 0 | 0 | 0 | 0 | 1 | 0 |
| AFE | 0 | 5 | 0 | 0 | 5 | 0 |
| CTT | 0 | 0 | 0 | 0 | 32 | 1 |
| PDAQ | 0 | 0 | 0 | 0 | 0 | 0 |
| Utility | 0 | 0 | 0 | 0 | 0 | 0 |
| To Right click to plot items | 0 | 30 | 0 | 0 | 50 | 5 |

Status: Connection to server started

Power Supplies: Good Alarms

CFT_LVAPE_PS6-A/+12V
CFT_LVAPE_PS6-A/+55V
CFT_LVAPE_PS8-B/+55V
CFT_LVAPE_PSD-A/+55V
CFT_LVAPE_PSD-B/+55V

SHOW
GUIDANCE
CONTROL
DISABLE
DISABLE ALL
CLOSE

***** CFT_LVAPE_PS8-B/+55V *****
Alarm cause: Low alarm
Alarm value: 5.917954
HiHi limit: 6.350000
High limit: 6.250000
Low limit: 5.900000
LoLo limit: 5.800000
Message contents:
version: v4
utility: ef(0,8)
timestamp: Wed Mar 7 02:06:09 2007
message type: alarm
name: CFT_LVAPE_PS8-B/+55V
priority: 0
host: d0o1ct158
db entry: 0
parent: none
children: none
transition: good
severity: minor
alarm type: analog
parameters: ai 6 5.917954 6.350000 6.250000 5.900000 5.800000

CLOSE DISABLE CONTROL GUIDANCE COMMAND

- Document all alarms! Save their types and actual values
- Make sure all disabled alarms are known

- **Front End Busy for crates 0x50–0x53**

- Pull out the 'CFT Trouble' checklist and follow the instructions
- Diagnose the problem and try to solve it
- Consult the 'Troubleshooting' section for more details

- **Temperature control alarms**

- If few alarms persist for more than 5 min, plot cryo and call the CFT expert
- Consult the 'Troubleshooting' section for more details

- **PDAQ alarms**

- If the rate of alarms is high ($\sim 5\%$) make a plot and call the CFT expert
- Consult the 'Troubleshooting' section for more details

- **Hot or cold regions in hitmaps**

- If the deviation from the reference is significant make a plot and call the CFT expert
- The typical numbers of hot and cold channels are in the checklist

- **Global platform power failure: CALL EXPERTS!**

General advice: Follow instructions in the checklists and on the web. Call expert when in doubt

- **Central Fiber Tracker Homepage:** Links to updates, references, contact info
<http://www-d0online.fnal.gov/groups/cft/>
- **Shifter's checklist companion and troubleshooting:** guidance for alarm and FEB problems
<http://www-d0online.fnal.gov/groups/cft/CFT/online/ChkListTrouble.html>
- **CFT shift schedule**
<http://hep.pa.msu.edu/cgi-bin/webcal/webcal.cgi?function=webmonth&cal=CFT>
- **Last 24 hours of CFT/CTT e-log:** Read it before your shift
https://www-d0online.fnal.gov/crlw/Index.jsp?inquiry=cft-ctt_24_hours

- CFT & PS are important parts of the D0 detector
- Make sure all CFT crates (0x50–0x53) are in the run even during quite time and Tevatron shutdowns
- Fill out the various forms at the appropriate times during your shift, starting with the 'Beginning of Shift' checklist and ending with the 'Shift Summary'
- Watch for CFT/CTT alarms. Do not leave major alarms unaddressed
- **Document all problems!**
- Do not hesitate to call the 'on-call' expert